

Figure 1 is a block diagram of a computer system 100. The computer system 100 includes a processor 102, a bus 101, a main memory 104, a read only memory 106, a mass storage device 107, a display 121, a keyboard 122, a cursor control device 123, and a camera 125. The processor 102 is connected to the bus 101. The main memory 104, read only memory 106, and mass storage device 107 are also connected to the bus 101. The display 121, keyboard 122, cursor control device 123, and camera 125 are connected to the bus 101 via a common bus line.

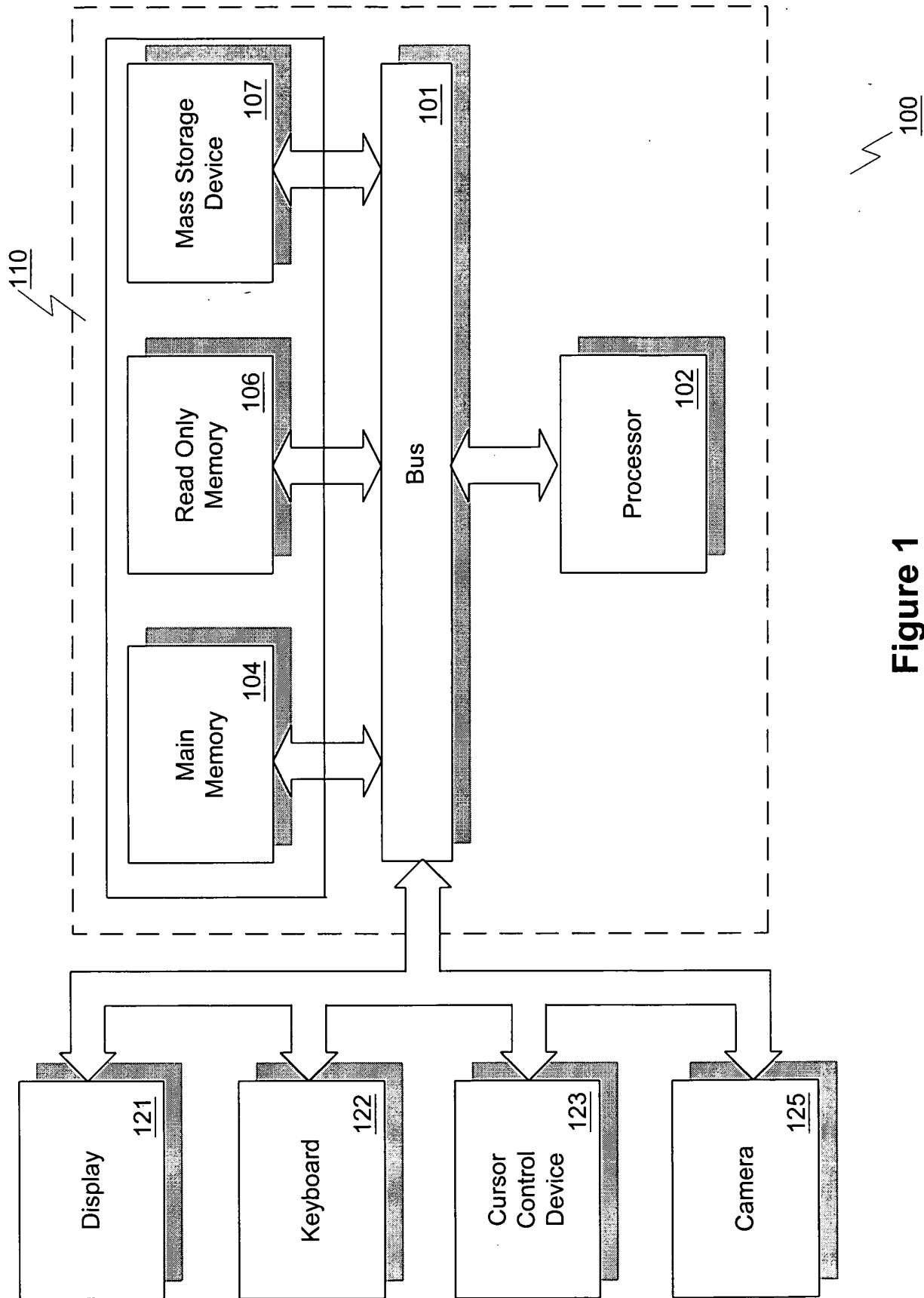


Figure 1

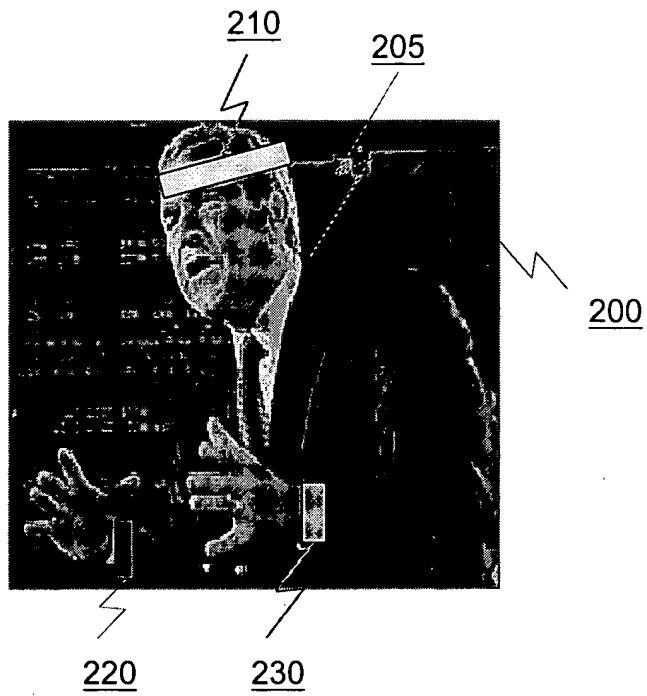


Figure 2

Figure 5 is a block diagram of a system for image quality assessment. The system includes an Image (505), a Preprocessor (510), a Quality Measure Generator (515), and an Image Adjustment Process (520). The Image (505) is input to the Preprocessor (510), which is input to the Quality Measure Generator (515), which is input to the Image Adjustment Process (520).

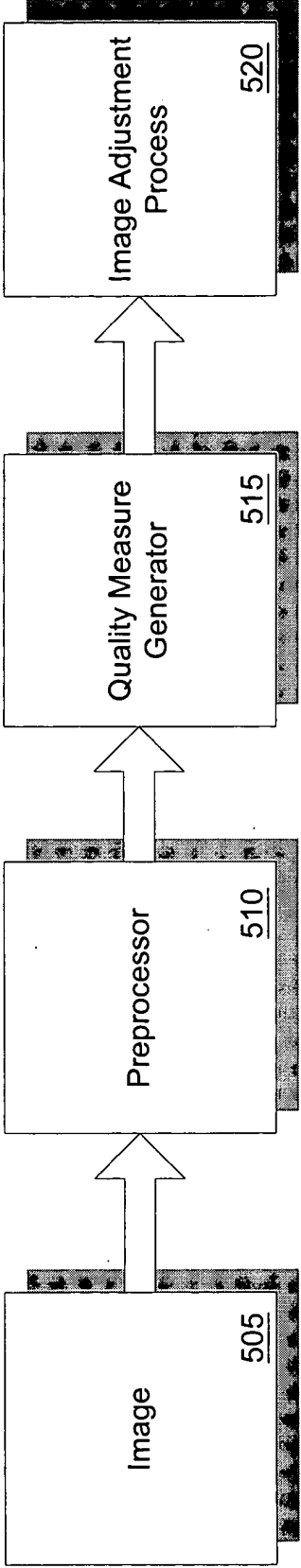


Figure 5

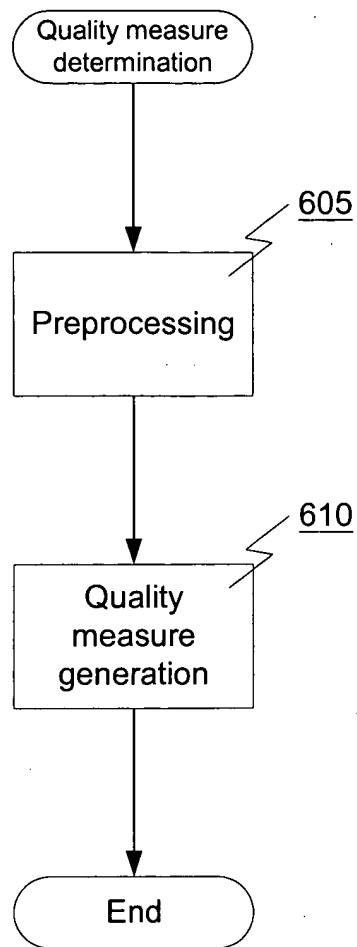


Figure 6

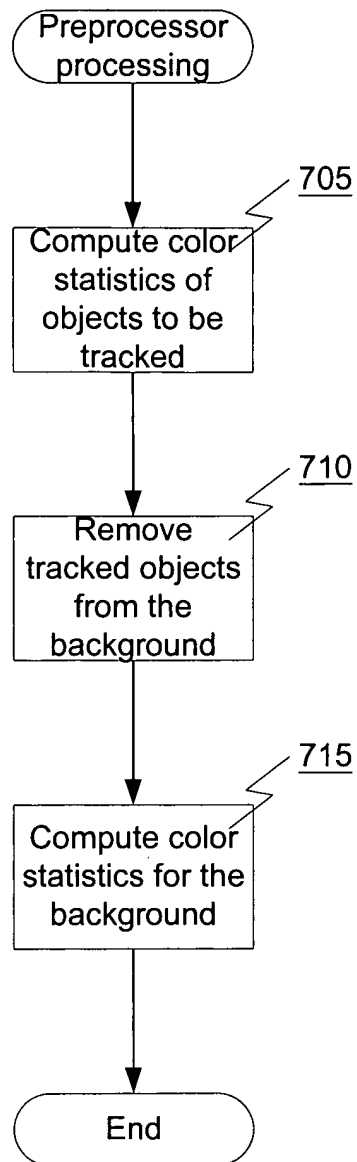


Figure 7

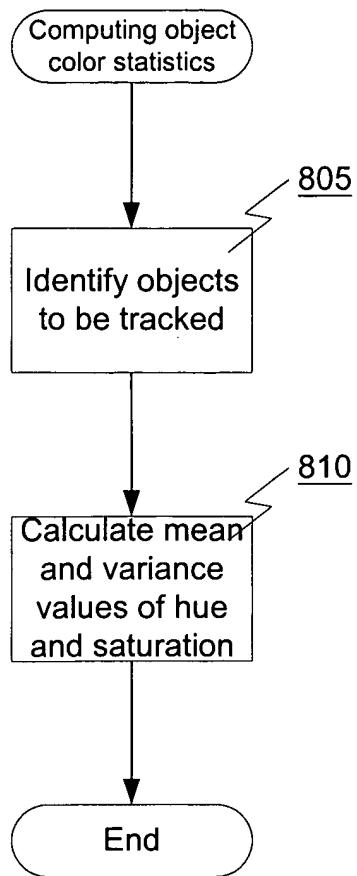


Figure 8

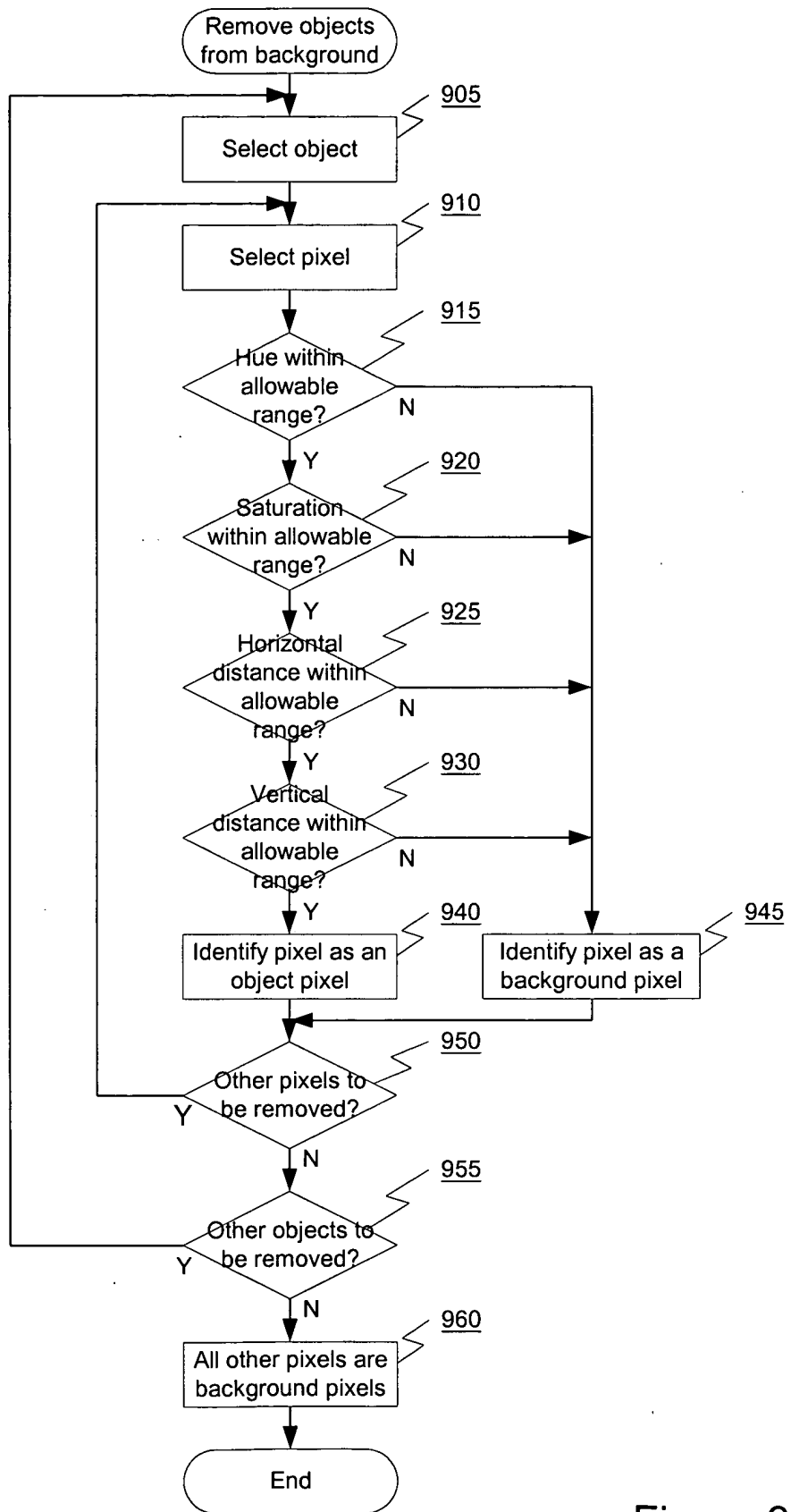


Figure 9

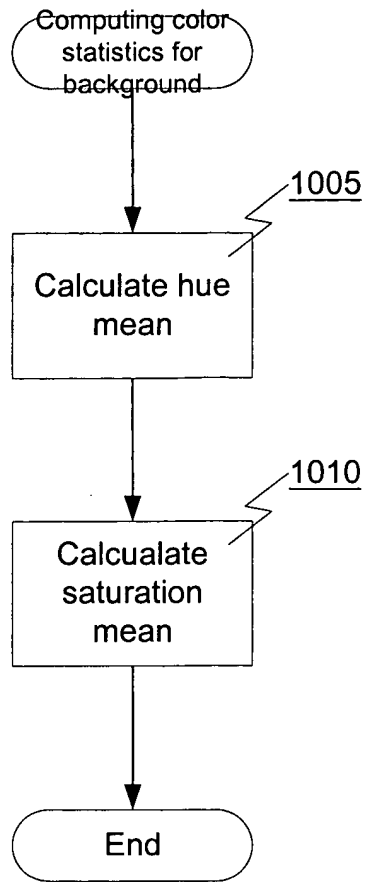


Figure 10

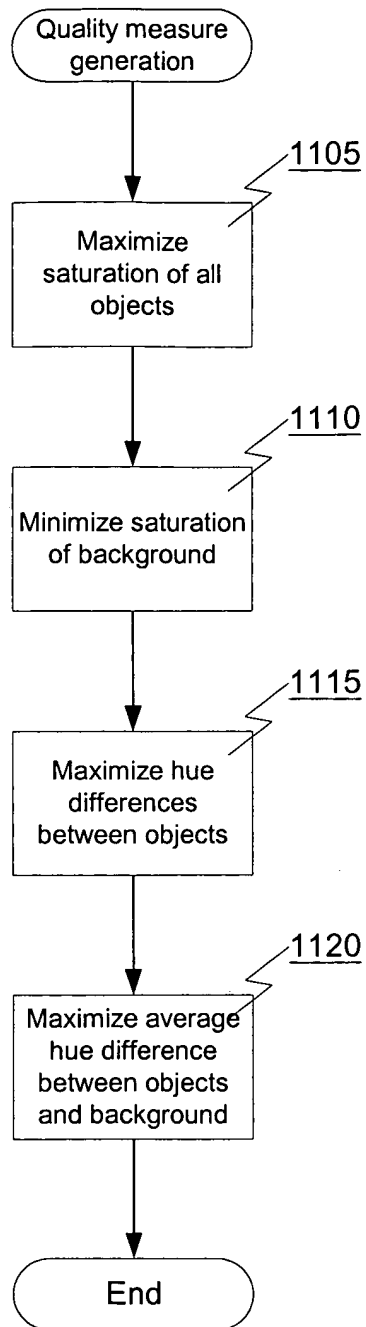


Figure 11

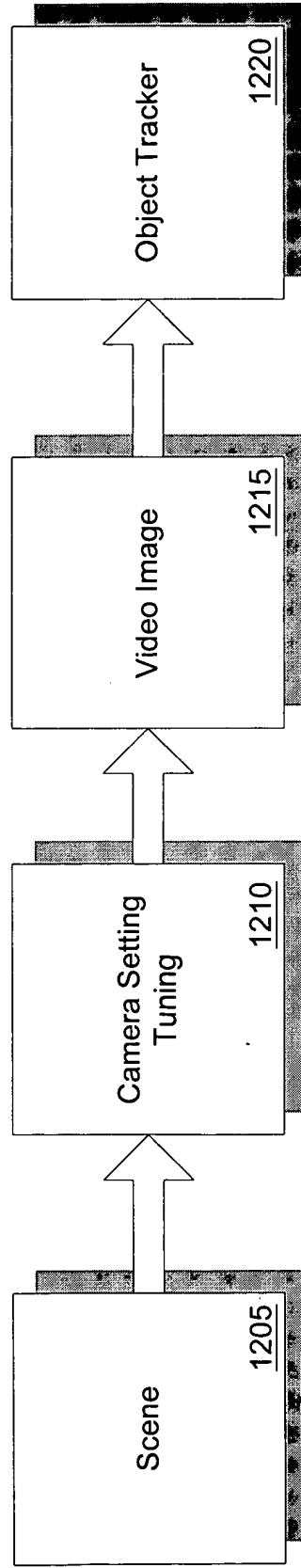


Figure 12

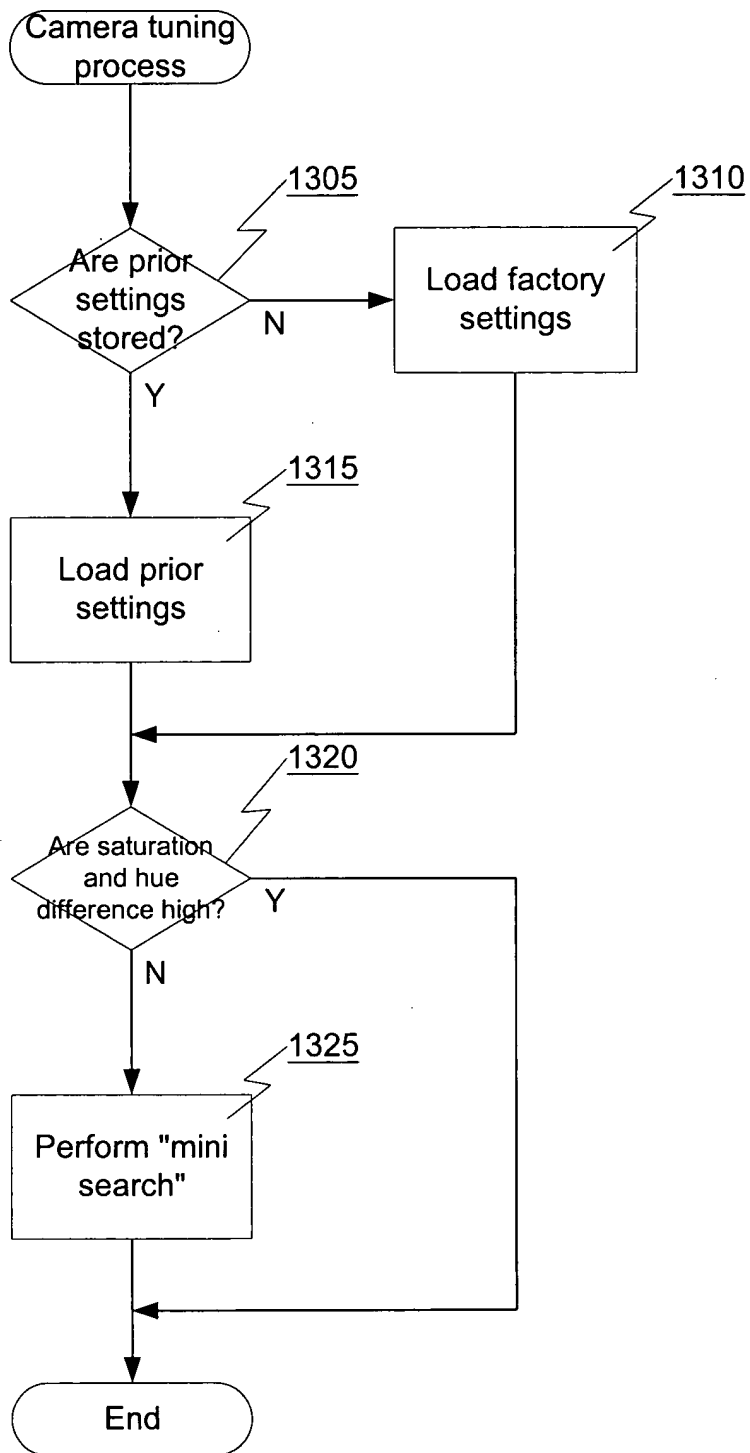


Figure 13

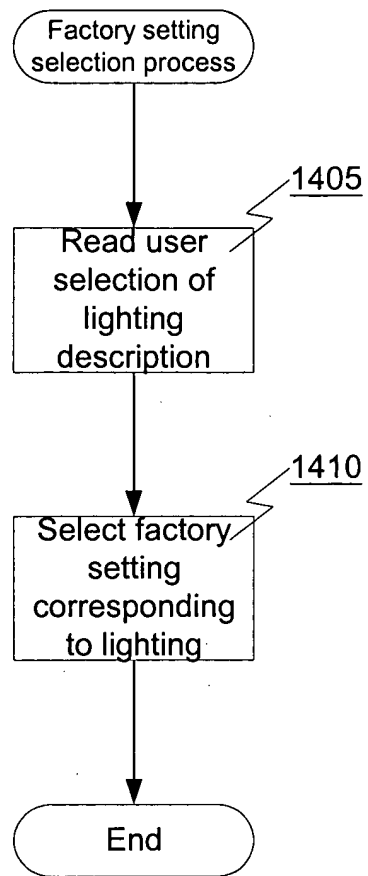


Figure 14

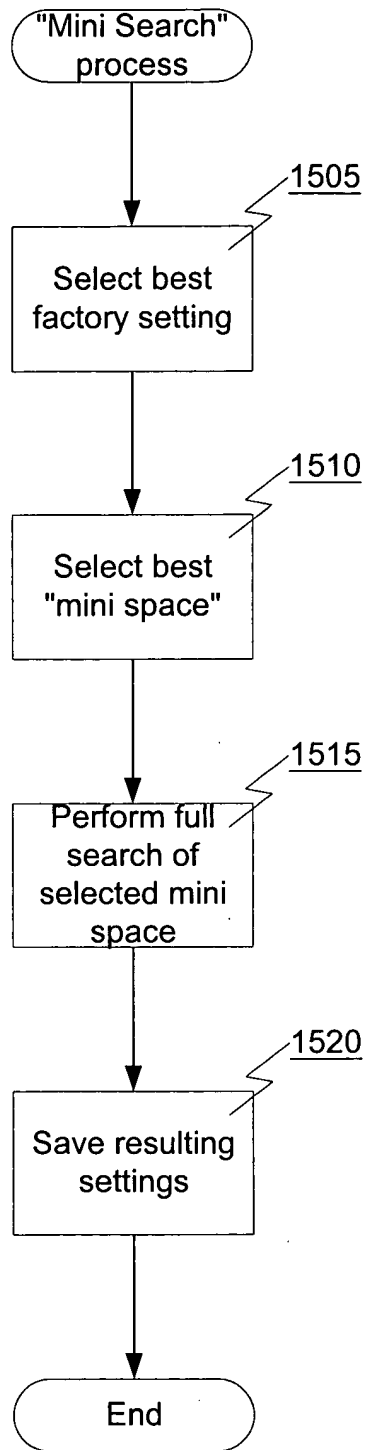


Figure 15

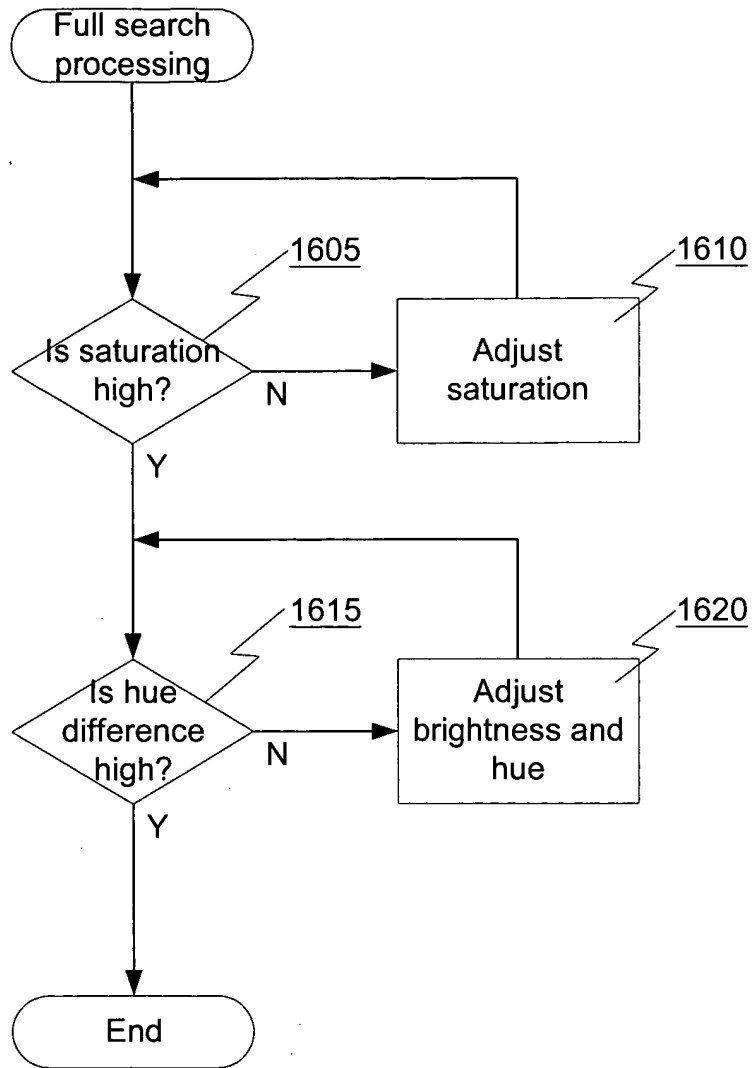


Figure 16

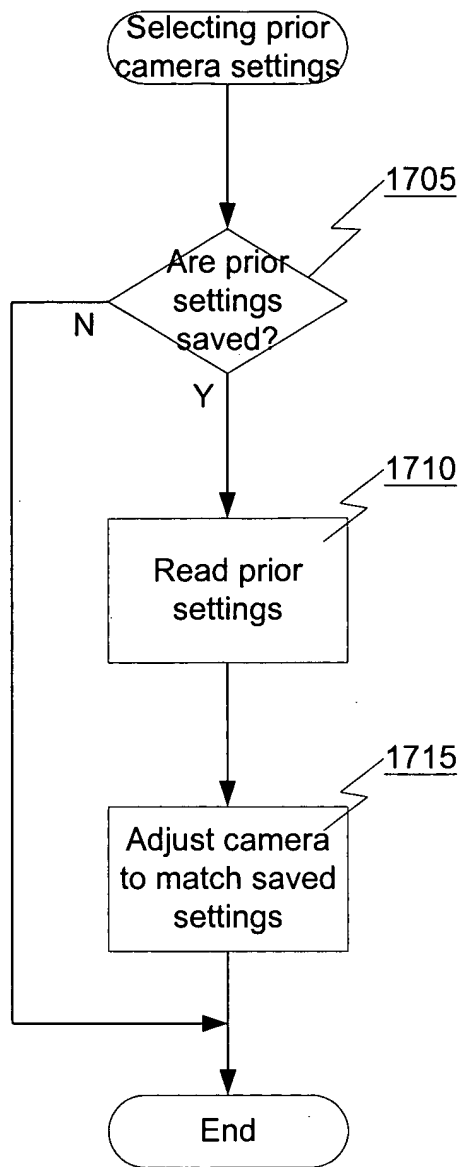


Figure 17